Ganesh Sriram

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EDUCATION:

SASTRA University   Thanjavur, India

B.Tech. Electronics & Communication Engineering   June 2008 – Present

Sri Chaitanya Junior College   Visakhapatnam, India

Senior Secondary Education   May 2006 – May 2008

SKILLS:

Programming Languages   C, C++, Java

Operating Systems   Windows 95/98/Me, Windows XP/Vista/7

Software   Matlab, MultiSim , PCB Design, Mathematica, LabVIEW

TECHNICAL EXPERTISE:

**Research Intern at Interconnect and Packaging center Georgia
Institute of Technology – Atlanta, USA**

Jan, 2012 - Present

Physical Design of 3D Integrated circuits.

**Short Term Course on Advances In VLSI Signal Processing -- IIT
Kharagpur** **21st – 25th December 2011**

- Understanding the latest hardware for signal processing
- Optimization at block level
  - Using techniques like retiming, pipelining, parallel processing, systolic arrays, folding, unfolding etc.
- Optimization at logic level
  - for multi objective functions involving speed, area and power
  - design of various datapath elements starting from high speed adders to complex CORDIC blocks

A

**Enhancing the security of LSB based image steganography using
encryption and gradient cluster based scrambling - Advanced Data
Research Institute, Department of Space**

- A standalone Steganography encoder and decoder.   **May, 2011 - Jun,**
- A novel algorithm using AES/DES encryption and gradient **2011** cluster based scrambling.
- Front end is a JAVA Swing based GUI while the back end was JAVA.

Understanding the various Wireless protocol available for data gathering - Thinklabs, SINE IITB   May, 2011 - May, 2011

- Understanding the various wireless protocols such as Zigbee, CC2500, Bluetooth, Wifi etc.
- Development of microcontroller based applications using various wireless protocols for swifter data communication.

Fabrication of Integrated Circuit from a silicon substrate - BEL, Bengaluru   Apr, 2011 - Apr, 2011

- This was a one day industrial visit where the cutting edge research going on in Bharat Electronics Labs was portrayed and a visit to the MSI fabrication Unit of BEL, Bengaluru was also arranged. The Fab is known for producing IC's such as 555 timer, Voltage regulators and hi voltage transistors. The entire process flow from conceptualization of an IC to its subsequent delivery in the form of a single chip with various packing's such as DIP, TQP etc, was shown.

The various patterns formed by a radar Echo and its subsequent classification based on human visual perception. - Gangavaram port, Visakhapatnam.   Dec, 2010 - Dec, 2010

- This was a 12 hour shift in the traffic control station of Gangavaram port in Visakhapatnam. Here the various echo patterns that were obtained were classified and various attributes were assigned to it. This shift showcased the various natural anomalies (previously encountered) and to classify a ship's cargo (tanker or container, etc.) based on its echo patterns.

PUBLICATIONS – REFREED JOURNALS

- Brinda Varatha Rajan, **Ganesh Sriram**, Praveen Elangovan, Sriram Damodharan and Vinoth Elangovan, "Performance Analysis of ANN based Spectrum Sensing Techniques in Cognitive Radio Networks" Accepted to the International Conference on "**Recent Advances in Computing and Software Systems**"(RACSS 2012), Chennai, Tamil Nadu, India.

SELECTED COURSES

ELECTRIC CIRCUIT THEORY, DIGITAL ELECTRONICS, ELECTRONIC CIRCUITS, ELECTROMAGNETICS, NETWORK THEORY, LINEAR INTEGRATED CIRCUITS, SIGNALS AND SYSTEMS, MICROWAVE ENGINEERING, ANTENNAS AND RADIOWAVE PROPAGATION, VLSI Design, Embedded Systems, Thin Film fundamentals, Introduction to MEMS and NEMS, FUNDAMENTALS OF NANOELECTRONICS.